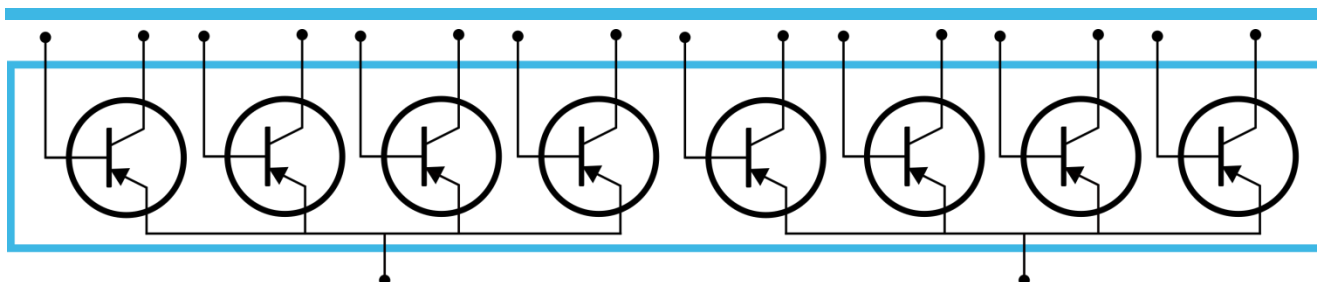
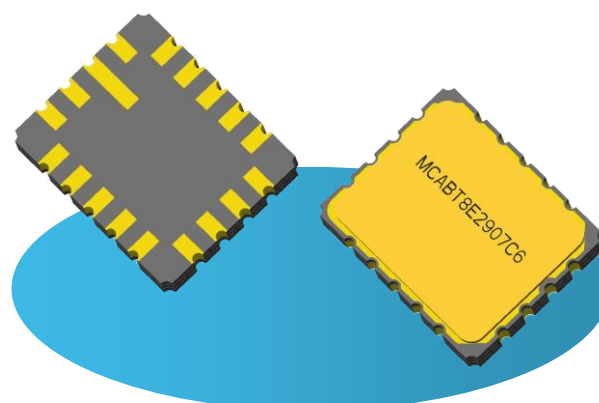


MULTI CHIP ARRAY 8 x COMMON EMITTER BIPOLAR 2N2907A TRANSISTORS

MCABT8E2907C6

- Hermetic MO-042AA (LCC6)
- Silicon Planar Epitaxial 8x 2N2907A PNP
- Transistors In A Common Emitter Array
- High Speed Low Saturation Switching
- High Reliability Screening Options Available



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise stated)

V_{CBO}	Collector - Base Voltage	-60V
V_{CEO}	Collector - Emitter Voltage	-60V
V_{EBO}	Emitter - Base Voltage	-5V
I_C	Continuous Collector Current	-600mA
$P_D^{(1)}$	Power Dissipation $T_{SP} = 25^\circ\text{C}$	2.18W
	Derate Above 25°C	12.5mW/ $^\circ\text{C}$
	Total Power Dissipation ⁽²⁾ $T_{SP} = 25^\circ\text{C}$	5.83W
	Derate Above 25°C	33.33mW/ $^\circ\text{C}$
T_J	Junction Temperature Range	-65 to $+200^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65 to $+200^\circ\text{C}$

THERMAL PROPERTIES

Symbols	Parameters	Max.	Units
$R_{\theta JSP}^{(1)(3)}$	Thermal Resistance, Junction To Solder Pad (Per Device)	80	$^\circ\text{C}/\text{W}$
$R_{\theta JSP}^{(2)(3)}$	Thermal Resistance, Junction To Solder Pad (Package)	30	$^\circ\text{C}/\text{W}$

Notes

- (1) One device conducting only.
- (2) With all parts conducting, maximum power dissipation per device = 729mW, limited by maximum junction temperature.
- (3) Stated $R_{\theta JSP}$ properties assume infinite heatsink.

Semelab Limited reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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MULTI CHIP ARRAY 8 x COMMON
EMITTER BIPOLAR 2N2907A TRANSISTORS
MCABT8E2907C6



2N2907A ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise stated)

Symbols	Parameters	Test Conditions	Min.	Typ.	Max.	Units
$V_{(BR)CEO}^{(4)}$	Collector - Emitter Breakdown Voltage	$I_C = -10\text{mA}$ $I_B = 0$	-60			V
$V_{(BR)CBO}$	Collector - Base Breakdown Voltage	$I_C = -10\mu\text{A}$ $I_E = 0$	-60			
$V_{(BR)EBO}$	Emitter - Base Breakdown Voltage	$I_E = -10\mu\text{A}$ $I_C = 0$	-5.0			
I_{CEX}	Collector - Emitter Cut - Off Current	$V_{EB} = -0.5\text{V}$ $V_{CE} = -30\text{V}$			-50	nA
I_{CBO}	Collector - Base Cut - Off Current	$V_{CB} = -50\text{V}$ $I_E = 0$			-10	nA
		$T_A = 150^\circ\text{C}$			-10	μA
$V_{CE(sat)}^{(4)}$	Collector - Emitter Saturation Voltage	$I_C = -150\text{mA}$ $I_B = -15\text{mA}$			-0.4	V
		$I_C = -500\text{mA}$ $I_B = -50\text{mA}$			-1.6	
$V_{BE(sat)}^{(4)}$	Base - Emitter Saturation Voltage	$I_C = -150\text{mA}$ $I_B = -15\text{mA}$	-0.6		-1.3	
		$I_C = -500\text{mA}$ $I_B = -50\text{mA}$			-2.6	
h_{FE}	Forward Current Transfer Ratio	$I_C = -0.1\text{mA}$ $V_{CE} = -10\text{V}$	75			
		$I_C = -1.0\text{mA}$ $V_{CE} = -10\text{V}$	100		450	
		$I_C = -10\text{mA}$ $V_{CE} = -10\text{V}$	100			
		$T_A = -55^\circ\text{C}$	50			
		$I_C = -150\text{mA}$ $V_{CE} = -10\text{V}^{(4)}$	100		300	
		$I_C = -500\text{mA}$ $V_{CE} = -10\text{V}^{(4)}$	50			

MULTI CHIP ARRAY 8 x COMMON EMITTER BIPOLAR 2N2907A TRANSISTORS MCABT8E2907C6



2N2907A DYNAMIC CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise stated)

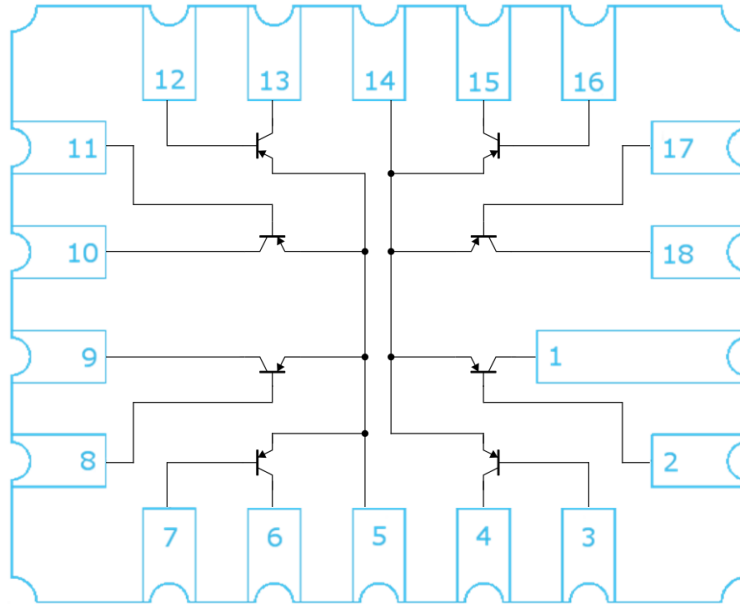
Symbols	Parameters	Test Conditions	Min.	Typ.	Max.	Units
$C_{obo}^{(5)}$	Output Capacitance	$V_{CB} = -10\text{V}$ $I_E = 0$ $f = 1.0\text{MHz}$			8	pF
$C_{ibo}^{(5)}$	Input Capacitance	$V_{EB} = -2.0\text{V}$ $I_C = 0$ $f = 1.0\text{MHz}$			30	
$ h_{fe} ^{(5)}$	Small Signal Current Gain	$I_C = -1.0\text{mA}$ $V_{CE} = -10\text{V}$ $f = 1.0\text{kHz}$	100			
$f_T^{(5)}$	Transition Frequency	$I_C = -50\text{mA}$ $V_{CE} = -20\text{V}$ $f = 100\text{MHz}$	200			MHz
$t_{on}^{(5)}$	Turn On Time	$V_{CC} = -30\text{V}$ $I_C = -150\text{mA}$ $I_{B1} = -15\text{mA}$			45	ns
$t_{off}^{(5)}$	Turn Off Time	$V_{CC} = -30\text{V}$ $I_C = -150\text{mA}$ $I_B = -I_{B2} = -15\text{mA}$			300	

Notes

- (4) Pulse Width $\leq 380\mu\text{s}$, duty cycle $\delta \leq 2\%$.
 (5) Characteristics by design.

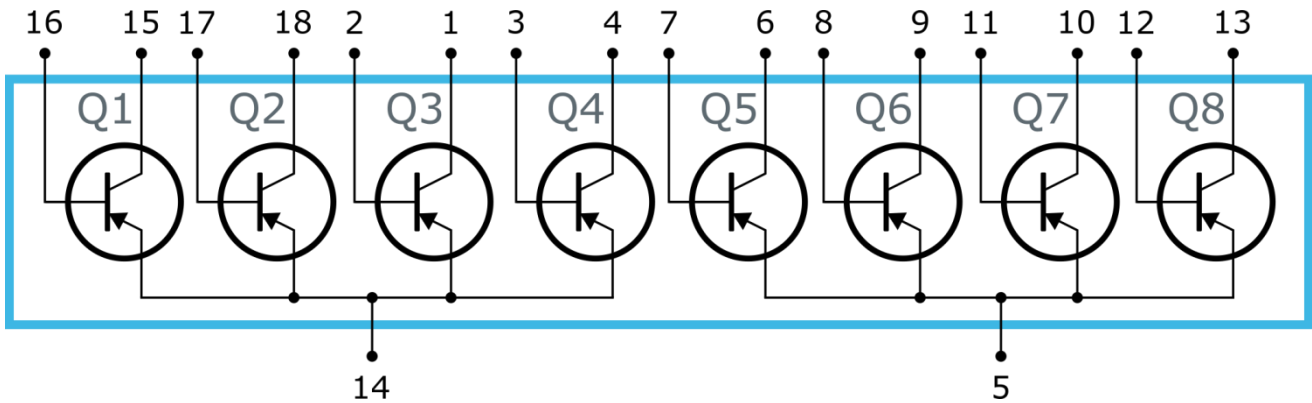
MULTI CHIP ARRAY 8 x COMMON EMITTER BIPOLAR 2N2907A TRANSISTORS MCABT8E2907C6

INTERNAL LAYOUT VISUALISATION



(Underside View)

PACKAGE PIN CONNECTIONS



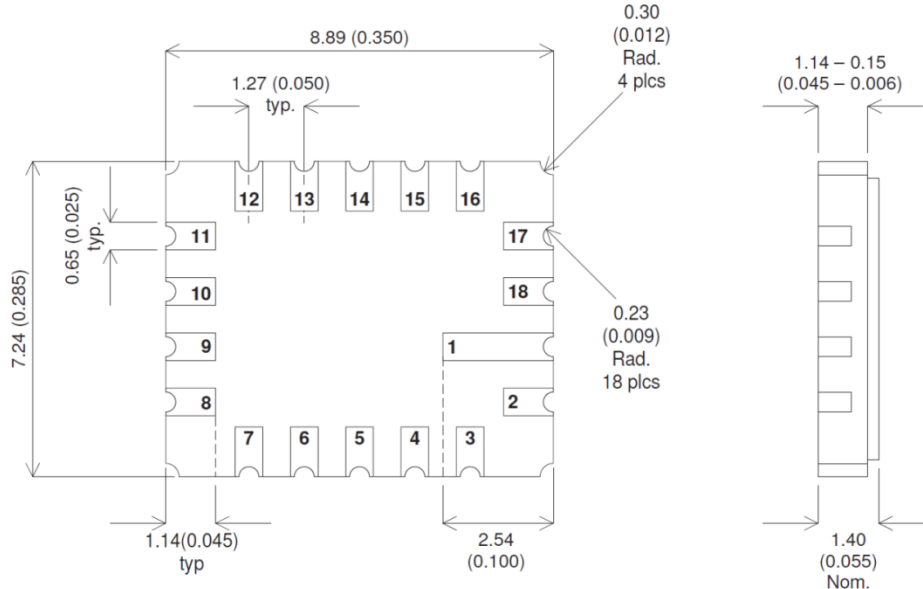
Pin	Device	Connection
1	Q3	Collector
2	Q3	Base
3	Q4	Base
4	Q4	Collector
5	Q5, Q6, Q7, Q8	Common Emitter
6	Q5	Collector
7	Q5	Base
8	Q6	Base
9	Q6	Collector

Pin	Device	Connection
10	Q7	Collector
11	Q7	Base
12	Q8	Base
13	Q8	Collector
14	Q1, Q2, Q3, Q4	Common Emitter
15	Q1	Collector
16	Q1	Base
17	Q2	Base
18	Q2	Collector

MULTI CHIP ARRAY 8 x COMMON EMITTER BIPOLAR 2N2907A TRANSISTORS MCABT8E2907C6

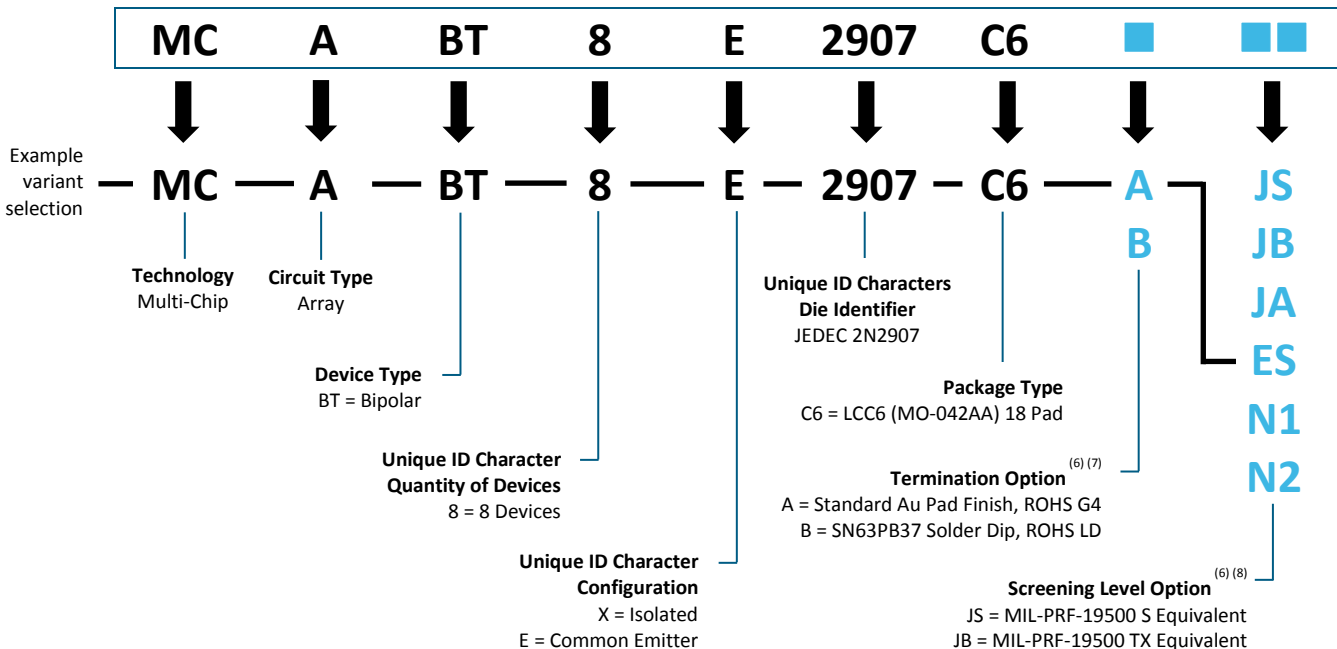
MECHANICAL DATA

Dimensions in mm (inches)



**C6 (MO-042AA)
(Underside View)**

PART VARIANT OPTIONS ⁽⁶⁾



Notes

- (6) Part variant options (termination, screening level) to be specified at point of order.
- (7) LD = e0, G4 = e4, as defined in J-STD-609 2nd Level Interconnect Category.
- (8) Please enquire with customer services regarding other requirements (pin connections, termination & screening level).